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CONFIRMATION NO. 3051

SERIAL NUMBER	FILING or 371(c) DATE	CLASS	GROUP ART UNIT	ATTORNEY DOCKET NO.
10/811,835	03/30/2004 RULE	439- 716	2825	030712-29

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**** CONTINUING DATA *********** FOREIGN APPLICATIONS *********** IF REQUIRED, FOREIGN FILING LICENSE GRANTED ****
06/07/2004

Foreign Priority claimed 35 USC 119(a-d) conditions met	<input type="checkbox"/> Yes <input checked="" type="checkbox"/> No <input type="checkbox"/> Yes <input checked="" type="checkbox"/> No	<input type="checkbox"/> Met after Allowance Initials	STATE OR COUNTRY	SHEETS DRAWINGS	TOTAL CLAIMS	INDEPENDENT CLAIMS
Verified and /HELEN ROSSOSHEK/ Acknowledged Examiner's Signature			JAPAN	6	15 9	9 2

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TITLE

Method of designing a circuit layout of a semiconductor device

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